

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Praveen Parvathala

Title: FUNCTIONAL RANDOM INSTRUCTION TESTING (FRIT) METHOD FOR COMPLEX DEVICES SUCH AS MICROPROCESSORS

Docket No.: 80107.051US1

Filed: July 31, 2001

Examiner: Unknown



Serial No.: 09/917661

Due Date: N/A

Group Art Unit: 2184

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Technology Center 2100

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- ☒ Revocation and Power of Attorney Statement under 37 CFR § 3.73(b) (2 pgs.).
- ☒ Status Inquiry (1 pg.).

LeMoine Patent Services, PLLC
c/o PortfolioIP
P.O. Box 52050
Minneapolis, MN 55402
952-473-8800

By: Dana B. LeMoine
Atty: Dana LeMoine
Reg. No. 40,062

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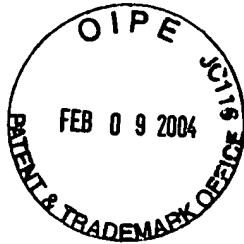
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Emily Bates
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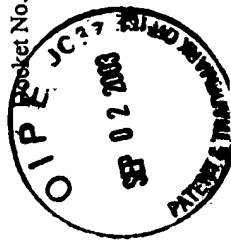
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